

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

872/FID
3/20/03
Suler

Applicant: M. Nandakumar et al. Examiner: Edgardo Ortiz
Serial No.: 09/876,292 Art Unit: 2815
Filed: 06/07/2001 Docket: T1-31089
For: ADDITIONAL N-TYPE LDD/POCKET IMPLANT FOR IMPROVING
SHORT-CHANNEL NMOS ESD ROBUSTNESS

March 18, 2003

Commissioner for Patents
and Trademarks
Washington, D.C. 20231

FAX RECEIVED

MAR 18 2003

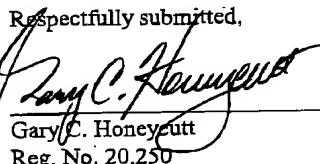
Sir:

TECHNOLOGY CENTER 2800

SUBSTITUTE DRAWINGS

Responsive to a telephone call from Examiner Ortiz, please substitute the enclosed formal drawings.

Respectfully submitted,


Gary C. Honeycutt
Reg. No. 20,250

Attorneys for Applicant
Godwin Gruber, LLP
Renaissance Tower
1201 Elm Street, Suite 1700
Dallas, Texas 75270-2084
Phone - 214.939.8651
Fax - 214.760.7332

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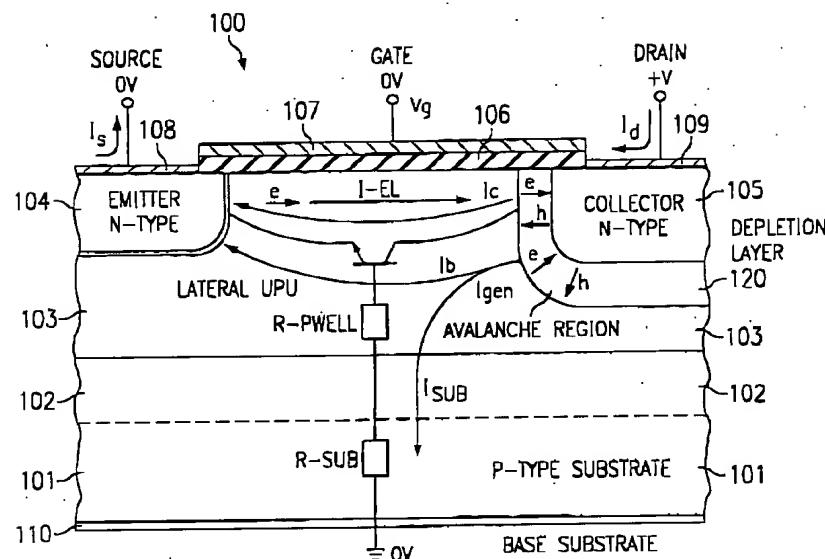


FIG. 1
(PRIOR ART)

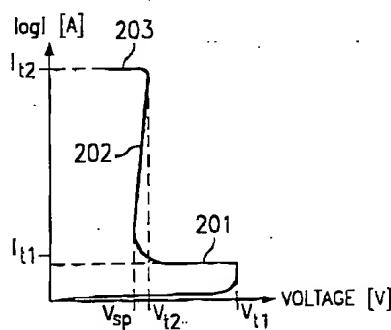


FIG. 2
(PRIOR ART)

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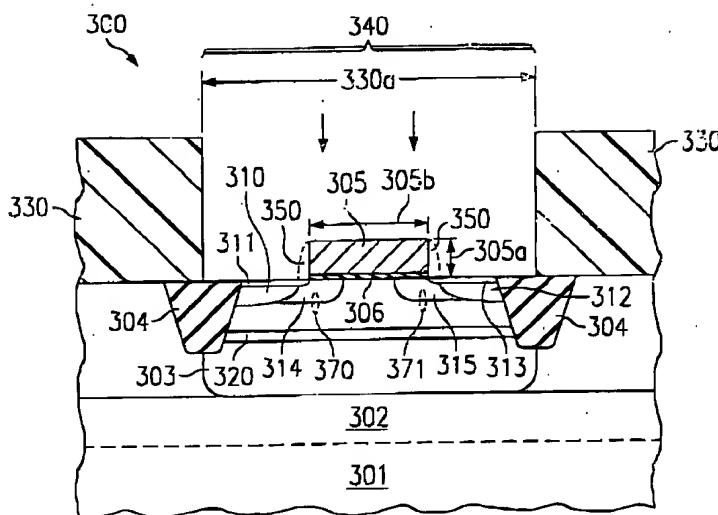


FIG. 3

310, 311 } S/D
312, 313 }
314, 315 } under
region
320 = Channel
stop

370, 371 = high
resistance

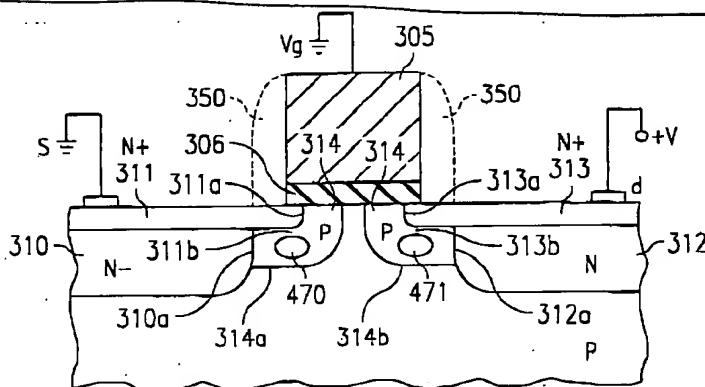


FIG. 4

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